

## Interfacing FlashRunner 2.0 with V850\_B devices

This also support two different protocols: CSI3WHS and UART1.

The algorithm support 2 different family: UX6LF & RC30F:

Regarding this algorithm, the most important difference between the 2 families UX6LF & RC30F are reported below:

- Different sclk entry (UX6LF → 100000, RC30F → 666000)
- RC30F has not a chip erase command, so the chip erase is performed with a loop or single block erase (0x8000 bytes for Code, 0x20 bytes for eeprom)
- UX6LF family can program the eeprom with *Id\_Tag bytes*, if the source file is created with *Id\_Tag*. - see [ID\_TAG][ ]
- The RC30F has not the *Id\_Tag* support, so there is a different verify eeprom for this family performed with read operations.
- Only RC30F family perform an unprotect, indeed there is the securityrelease command (0xA2)

Additional Parameters List:

- SIPU (SI Pull Up Enable) – Enable/Disable SI software pull up - *Only for CSI3WHS Protocol*
- SOPU (SO Pull Up Enable) – Enable/Disable SO software pull up - *Only for CSI3WHS Protocol*
- ID\_TAG (Eeprom ID\_TAG feature) - Select YES if the source file has been produced with ID\_TAG feature - see [ID\_TAG][ ]

Below the *granularity* Manual Commands - *ALIGNMENT*:

| COMMAND        | UX6LF                  | RC30F                     |
|----------------|------------------------|---------------------------|
| BLOCKERASE     | 0x8000 (32768 bytes)   | 0x20 (32 bytes)           |
| BLANKCHECK     | 0x400 (1024 bytes)     | 0x400 (1024 bytes)        |
|                |                        |                           |
| PROGRAM/VERIFY | 0x10 (16 bytes) Flash, | 0x400 (1024 bytes) Flash, |
|                | 0x04 (4 bytes) EEprom  | 0x02 (2 bytes) EEprom     |
|                |                        |                           |
| READ/DUMP      | 0x10 (16 bytes) Flash  | 0x10 (16 bytes) Flash,    |
|                | 0x04 (4 bytes) EEprom  | 0x02 (2 bytes) EEprom     |

### UNPROTECT command

The UNPROTECT command is available only for RC30F family. Use it after the MASSERASE command in order to activate the Security programming.

### HCU Hexfile

The HCU Hexfile is a specific format for .rec files for RL78, V850ES,E1 V850E2R, V850E2K Renesas Families.

Further informations on HEX\_Utility\_HCU\_SDM\_for\_EL\_FP\_Service\_IDS-4903-03E\_.pdf provided by Renesas after a support request.

The document reports the informations about Option Data definitions.

Starting from version 2.00 that format can be used directly, without changing addressing for Option Bytes and OCDID, indeed the memory maps are different: Offset has been removed and the new address is defined inside the HexFile

| MEMORY       | BEFORE 2.00 | VERSION 2.00-HCU Hexfile |
|--------------|-------------|--------------------------|
| Option Bytes | 0xFF000000  | 0xF00028                 |
| OCD          | 0xFF100000  | 0xF00018                 |

## ID\_TAG

There is **only 1 flash\_tech** that support the **EEProm ID\_TAG** feature: UX6LF.

There are two different ways to program this memory type:

- **WITH\_ID\_TAG** by checking ID\_TAG as optional parameter
- **WITHOUT\_ID\_TAG** by un-checking ID\_TAG
- The ID\_TAG value is related with the source file - it is a compiler option

See the table below to better understand what does ID\_TAG do, with an example of 8byte programming.

| FRB               | ID_TAG YES        | ID_TAG NO                              |
|-------------------|-------------------|----------------------------------------|
| 00010203 04050607 |                   |                                        |
|                   | 00010203 04050607 | 00010203 FFFFFFFF 04050607<br>FFFFFFFF |

Basically, while programming with ID\_TAG, the size is always double because there is a **FFFFFFFF** involved. See also Read operation.

## Commands List

Memory types:

F → Flash

E → EEprom

O → Option Bytes

D → OCD ID

### MASSERASE

**MASSERASE** <F|E|C>

This function perform a Masserase for F, E or whole memory

### BLOCKERASE

**BLOCKERASE** <F|E> <start\_addr> <size>

Single blockerase for F or E

### BLANKCHECK

**BLANKCHECK** <F|E> or **BLANKCHECK** <F|E> <start\_addr> <size>

Blankcheck command for Flash or Eeprom - Auto or Manual mode allowed

### PROGRAM

**PROGRAM** <F|E|O|D> or **PROGRAM** <F|E> <start\_addr> <size>

Program command for F, E, O or D - Auto or Manual mode allowed.

The O & D memory type can be only Auto.

### VERIFY

**VERIFY** <F|E|O|D> <R|S> or **VERIFY** <F|E> <R|S> <start\_addr> <size>

Verify command for F, E, O, D - Auto or Manual mode allowed.

R – Normal verify with Read-Out method

S – Normal verify with Checksum method

### READ

**READ** <F|E> <start\_addr> <size>

Read function for F, E – Address check

### DUMP

**DUMP** <F|E> or **DUMP** <F|E> <start\_addr> <size>

Dump function for F, E – Address check

## RUN

Run in User Mode command

## PROTECT

**PROTECT** <security\_byte\_0> <security\_byte\_1> <security\_word\_0>  
<security\_word\_1>

This command protect only the F with 6 security bytes

## UNPROTECT

This command clears the flash option data. *Available only for RC03F family*

## GET\_SIGNATURE

This command return the signature buffer (32bytes)

## GET\_OB

This command return the Option Bytes (36bytes – UX6LF family, 4bytes – RC03F family)

## GET\_OCDID

This command return the OCD\_ID Bytes (12bytes – UX6LF & RC03F family)

## GET\_SECURITY

This command return the security Bytes (6bytes – UX6LF & RC03F family)

## CONNECT

Connect function: Power on and entry

## DISCONNECT

Disconnect function: Power off and exit